

# CS/CoE 535

## Acceleration of Networking Algorithms in Reconfigurable Hardware

### Lecture 14

Washington University  
Fall 2001

<http://www.arl.wustl.edu/~lockwood/class/cs535/>

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## Announcements

- Midterm Exam
  - Covered Material
  - Location, Date & Time
  - Allowed and Disallowed items

## CS535 Midterm Exam

- **Material**
  - All topics in machine problems
  - All topics in homework
- **Date/Time**
  - Monday, November 19
  - 4:00pm - 5:30pm
- **Location**
  - This classroom in Couples 1
  - NOT in lab

## CS535 Midterm Exam (continued)

- **Allowed**
  - One page of your own notes
    - Two sides of 8.5" x 11" sheet
  - One page of VHDL references
    - Two sides of 8.5" x 11" sheet
- **Not Allowed**
  - No books
  - No computers
  - No cell phones, PDAs, or FPGAs

## CS535 Midterm Exam (continued)

- Test goals
  - Ensure that you understand how to implement networking hardware modules in hardware
  - Ensure that you understand the material covered in the machine problem