

CS/CoE 535

Acceleration of Networking Algorithms in Reconfigurable Hardware

Lecture 16

Washington University
Fall 2001

<http://www.arl.wustl.edu/~lockwood/class/cs535/>

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Lockwood@arl.wustl.edu

Announcements

- MP4 Returned
- MP5 Deadline Extended
 - Midnight Tonight (Wed, Nov 14)
- Final Project Signup
 - Deadline: Midnight Tonight (Wed, Nov 14)
 - See Lecture 15 for other deadlines
- Midterm Exam Reminder
 - Cupples 1 classroom (not lab) on Monday!
 - See Lecture 14 for details

Announcements (2)

- Today is the final lecture of this course!
 - All remaining Lectures will focus on projects
 - Congratulations on your hard work throughout the semester!
 - Applications of your work
 - **Implement faster network hardware (Cisco, Startups)**
 - **Add features and security for next-generation Internet**
 - Where to go from here
 - **Industry**
 - **Independent Study (Spring 2002)**
 - Independent Study (E61-400-16)
 - Senior Project (E61-494-16)
 - **Graduate Study (MS/PhD)**
 - Graduate Study (E61-500-16)
 - Masters Project (E61-598-16)
- Course Evaluations

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Prof. Lockwood : Fall 2001

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Your Final Project Title

Your project URL goes here

Your Project Team Member Names go here

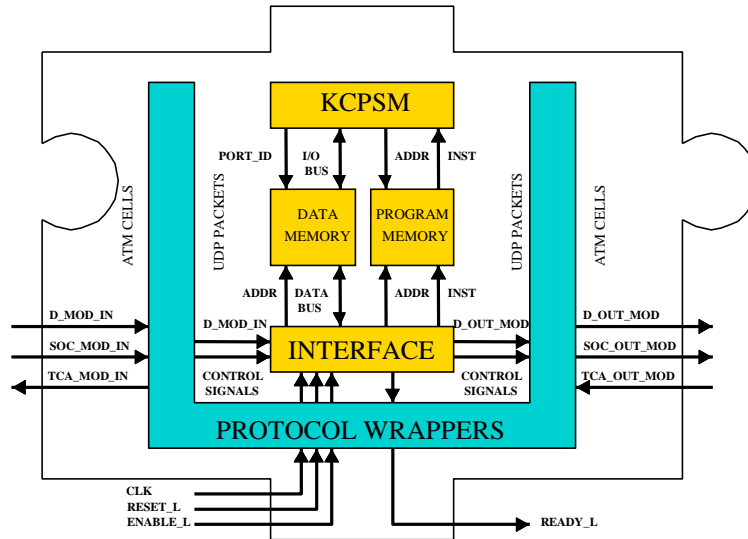
Project Background

- Describe the context of the problem
- Why is the problem important?
- Who would use it?

Project Description

- What is the goal of your project?
- How will it work?
- Who would use it?

Final Project Block Diagram



Data Formats

- Data Inputs
 - Control cell format ?
 - Frame format ?
 - UDP/IP packets ?
- Data Outputs
 - Control cell format ?
 - Frame format ?
 - UDP/IP packets ?

Major Components

- Module Function
- Data Interface
 - Inputs
 - Outputs
- Name of person responsible
- Timeline for implementation
 - X finished by T1
 - Y finished by T2

Implementation Plan

- System Design
 - Hardware Components
 - Software Components
- Hardware Implementation
 - VHDL Coding
- System Integration
- Simulation
 - Testbench Design
- Synthesis
- Hardware Debugging
 - How will you demonstrate the system working?

Status Report

- What component ?
- What does it do?
- What is left to complete?

Results

- What does it really do?
- How fast does it go?
- How much logic / memory does it require?
- How did you test it?

References

- Relevant References
 - Search www.ResearchIndex.com for related papers
 - Search www.google.com for related work
- What components did you use?
- What new components did you implement?