

# Advanced Computer Systems Architecture

## Chip-Multiprocessors: Applications and Architectures

CSE 526M

Prof. Patrick Crowley

## Plan for Today

- Source code turn-in
  - Email or CDR
  - Include a README telling me how to run a simulation
- Final exam slot
  - Monday, May 10
  - 1-3pm, Lab Sciences 301
- Questions
- Today's presentations

## Project Logistics

- Dates
  - Today's date: Apr 13
  - End date: Thursday, April 15
- Weekly Milestones

|           |               |                                  |
|-----------|---------------|----------------------------------|
| <i>M1</i> | <i>Mar 4</i>  | <i>Implementation 1</i>          |
| <i>M2</i> | <i>Mar 18</i> | <i>Implementation 2</i>          |
| <i>M3</i> | <i>Mar 25</i> | <i>Implementation 3</i>          |
| <i>M4</i> | <i>Apr 1</i>  | <i>Implementation Wrap-up</i>    |
| <i>M5</i> | <i>Apr 8</i>  | <i>Plan future work, Reports</i> |
| <i>M6</i> | <i>Apr 15</i> | <i>Presentations</i>             |

## Assignment

- Thursday (4/15)
  - Final 3 presentations
    - FAM/JackHMMer
    - FPLib
    - TCP Processor
- Tuesday (4/20):
  - **Commentary:** A Preliminary Architecture for a Basic Data-Flow Processor
  - 15 minute perspective: FPLib