1. The simulation output below shows the control signals from the basic processor during the execution of five instructions. Each instruction is “boxed” for clarity. For each instruction, write the name of the instruction being executed. The list of instructions appears on the reverse side of the page.
0xxx  load ACC with value stored in memory word xxx
1xxx  store value in ACC into memory word xxx
2xxx  add value in memory word xxx to value in ACC
3000  negate the value in ACC
3001  halt
4xxx  change the value of PC to xxx
5xxx  if the value of ACC is zero, change PC value to xxx
6xxx  load ACC with value whose address is stored in word xxx
7xxx  store ACC value into word whose address is in word xxx
8xxx  change ACC value to xxx
9xxx  add xxx to the value in ACC