Advanced Computer Systems Architecture

Chip-Multiprocessors: Applications and Architectures

CSE 526M
Prof. Patrick Crowley

Plan for Today

• Source code turn-in
  – Email or CDR
  – Include a README telling me how to run a simulation
• Final exam slot
  – Monday, May 10
  – 1-3pm, Lab Sciences 301
• Questions
• Today’s presentations
Project Logistics

• Dates
  – Today’s date: Apr 13
  – End date: Thursday, April 15
• Weekly Milestones

<table>
<thead>
<tr>
<th>Milestone</th>
<th>Date</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>M1</td>
<td>Mar 4</td>
<td>Implementation 1</td>
</tr>
<tr>
<td>M2</td>
<td>Mar 18</td>
<td>Implementation 2</td>
</tr>
<tr>
<td>M3</td>
<td>Mar 25</td>
<td>Implementation 3</td>
</tr>
<tr>
<td>M4</td>
<td>Apr 1</td>
<td>Implementation Wrap-up</td>
</tr>
<tr>
<td>M5</td>
<td>Apr 8</td>
<td>Plan future work, Reports</td>
</tr>
<tr>
<td>M6</td>
<td>Apr 15</td>
<td>Presentations</td>
</tr>
</tbody>
</table>

Assignment

• Thursday (4/15)
  – Final 3 presentations
    • FAM/JackHMMer
    • FPLib
    • TCP Processor
• Tuesday (4/20):
  – Commentary: A Preliminary Architecture for a Basic Data-Flow Processor
  – 15 minute perspective: FPLib